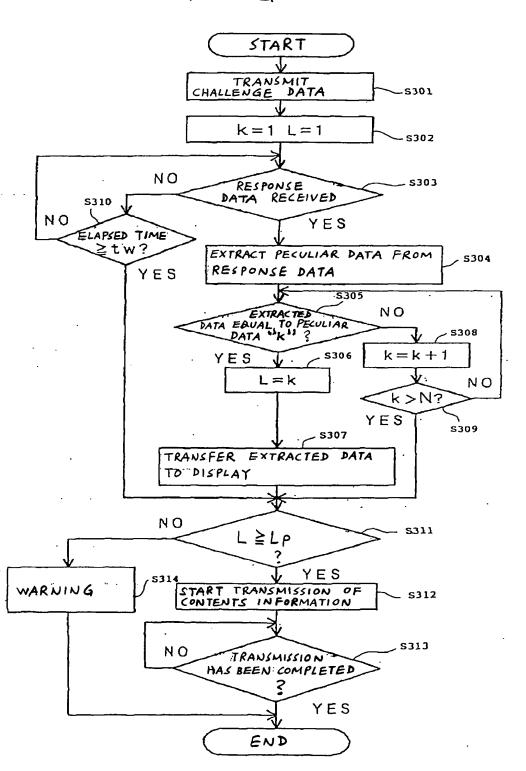
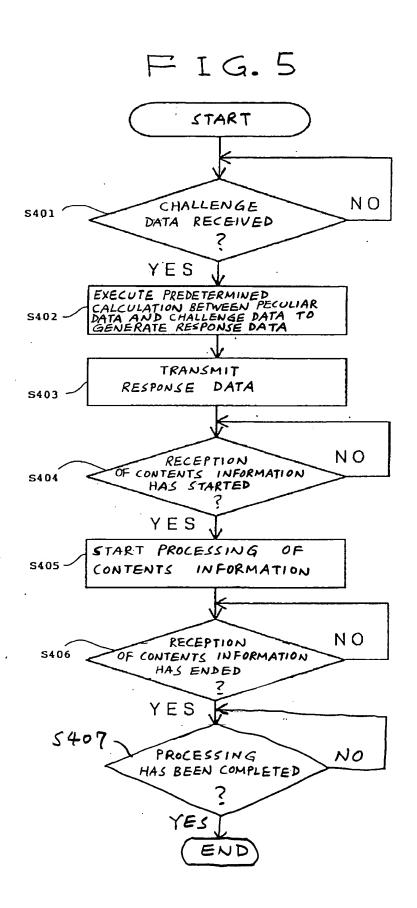


FIG. 4

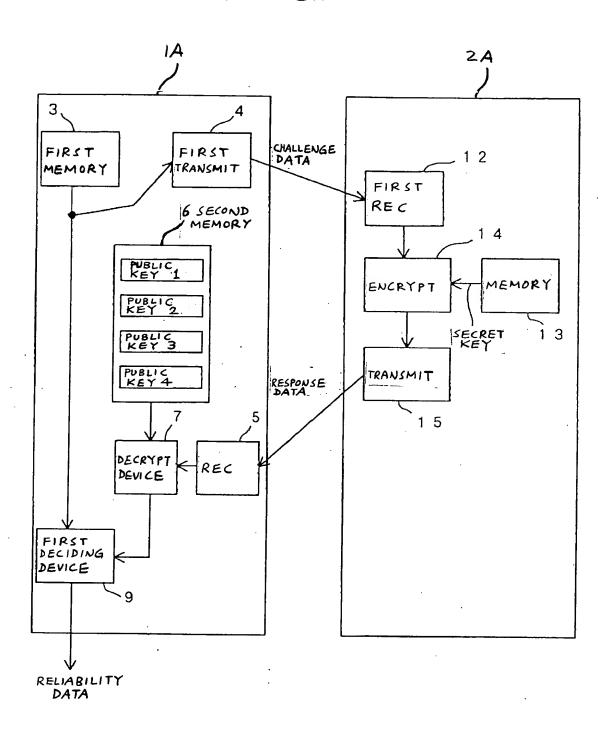




F1G.6

14 9 MEMORY TRANSMIT HENCRYPT SECOND REC Ø FIRST REC D 2 A 0 တ DECIDING DEVICE SECOND DECIDING DEVICE TRANSMIT CONTROL SECOND FIRST 7 MEMORY FIRST TRANSMIT MEMORY SECOND FIRST DECRYPT DEVICE REC က (4 (Ú 9 /

FIG. 7



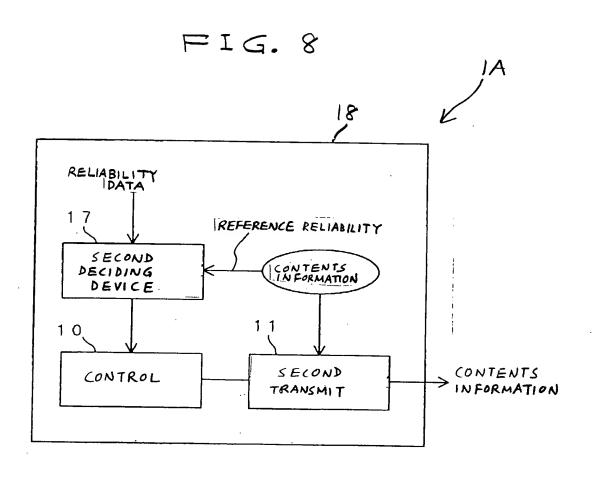


FIG. 9

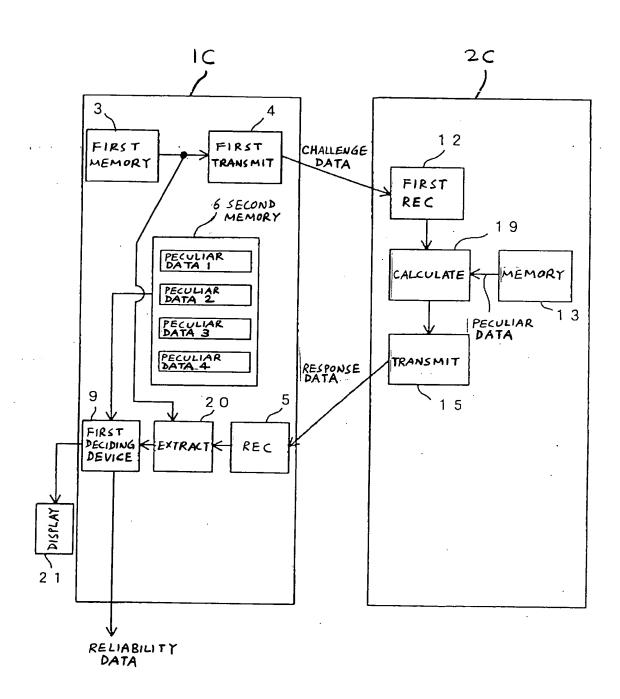


FIG. 10

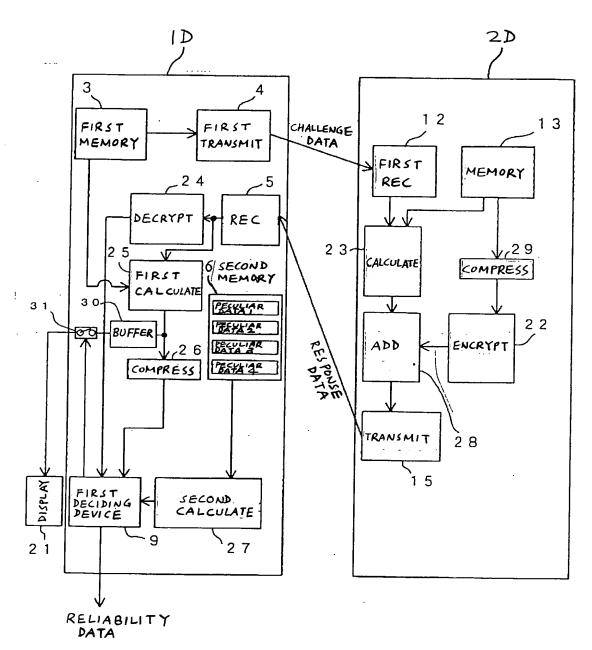
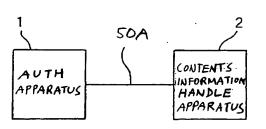


FIG.11

FIG. 12



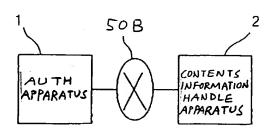


FIG. 13

